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UNITED STATES PATENT APPLICATION
FOR

METHOD FOR HYBRID NETWORK PLANNING

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1 **CROSS-REFERENCE TO PROVISIONAL APPLICATION**

2 This application claims the benefit of U.S. Provisional Application Ser. No.
3 60/113,168, filed December 21, 1998.

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6 **FIELD OF THE INVENTION**

7 Embodiments of the present invention provide methods for planning a path through a
8 network and for managing network capacity. More particularly, embodiments of the present
9 invention provide methods for planning a path and managing capacity on a network in which
10 the links can carry multiple channels.

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13 **BACKGROUND OF THE INVENTION**

14 A network planning tool performs capacity planning at a systems level of traffic on a
15 network, such as a fiber optic network, and a capacity management router performs network
16 capacity management. For example, an Internet services communications company such as
17 UUNET® may request to purchase use of a certain amount of network bandwidth on a fiber
18 optic network for a period of time, such as use of an OC12 line from Fairfax, Virginia to New
19 York City for a one-year period beginning three months in the future. A network planning
20 tool analyzes the network capacity already scheduled for that time period and finds the best
21 path through the network that meets the request. Typically, the network planning tool
22 attempts to find the path which meets the customer's demands but uses as little network
23 resources as possible. A capacity planning router plans for future expansion of the network
24 based on anticipated future usage. If the capacity planning router determines that future
25 bandwidth demand will overburden a particular part of the existing network, the network

1 managers can consider adding new equipment to the network or "lighting" a fiber that is
2 currently "dark."

3 A communication network contains a set of network structures with different
4 topologies, such as mesh, closed ring, open ring (also known as linear chain), and point-to-
5 point. Typically, a network structure contains a set of nodes each of which are connected by
6 links to one or more of the other nodes. A link may contain, for example, one or more fiber-
7 optic cables. In a fiber optic system, a node may be a cross-connect system, which can route
8 signals, and may also be an add-drop multiplexer (ADM), which provides a network
9 multiplexing function and is used to add/drop signals for the links. A network structure is a
10 set of ADM nodes that are connected with each other through one or more links. A link may
11 carry multiple channel signals. Network structures are inter-connected with each other
12 through a cross-connect node or switching node. For example, the Synchronous Optical
13 Network (SONET) and Synchronous Digital Hierarchy (SDH) network contain a set of
14 SONET network structures such as SONET ADM rings and SONET point-to-point systems.
15 SONET network structures may be connected with each other through a SONET cross-
16 connect system (SONET DCS). Another example of a communication network is an optical
17 network containing a set of optical network structures. Each optical structure could be an
18 optical ADM ring or optical point-to-point system. Optical network structures may be
19 connected through an optical cross-connect system or optical switching system.

20 Each link in a network structure may be able to simultaneously carry different signals
21 over different channels. In a network using time division multiplexing, several low-speed
22 signals may be combined to form a single high-speed signal by dividing the high-speed signal
23 up into different time slots. A SONET/SDH network is an example of a time division
24 multiplexing network. Such a low-speed signal satisfies a Time Slot Assignment (TSA)

1 constraint in the ADM node when the signal traverses the network structure in the same slot.
2 If a single slot is unavailable throughout the entire route within that structure, the low-speed
3 signal needs to change from one slot to another by dropping out of the high-speed signal,
4 leaving the ADM node (as well as this structure), entering a slot exchange terminal (such as
5 the cross-connect node), and adding back into a new slot of the high-speed signal at the ADM
6 terminal. Due to the time slot assignment constraint, such a slot change external to an ADM
7 node is considered an expensive "hair pin" solution for transmitting a signal through different
8 slots within a structure. In a system that uses dense wavelength division multiplexing
9 (DWDM), a number of signals each using a different wavelength may be sent though the
10 same optical cable at the same time. Such a signal may be sent at the same wavelength across
11 all fibers that it traverses within that optical structure, in which case it satisfies a wavelength
12 continuity constraint, or it may switch wavelengths. Optical cross-connect nodes are
13 equipped with wavelength conversion equipment and may be used to connect these optical
14 structures.

15 A network may contain, for example, more than ten thousand nodes and twenty
16 thousand links. Because there can be millions of possible paths through such a network, it
17 may be difficult to find the shortest path from a source node to a destination node. This
18 problem is made more complicated because, as discussed above, each link in the network may
19 have multiple channels and there are additional cost and equipment availability issues
20 involved where network traffic changes from one channel to another channel. Thus, finding
21 the best path through a network requires taking into account the availability of the different
22 channels in the network.

23 Previous network planning tools and capacity management routers used techniques
24 such as linear programming or mixed integer programming to determine the least cost path

1 through the network. Such planning and management tools used a two step approach to
2 planning a least cost path that involved initially determining a path through the network
3 without regard to the availability of the corresponding channels in the links selected. Only
4 after choosing a candidate route from a source to a destination would these systems consider
5 whether corresponding channels were available in the links chosen. Such systems did not
6 consider channel availability when performing the first step (i.e., when determining a
7 candidate route) because such a consideration would make the determination too complex.

8 It would be advantageous to have a network planning and management tool that
9 efficiently considers the availability of channels and the cost of channel interchange when
10 determining the best path through the network, thereby performing routing and channel
11 assignment at the same time.

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13 SUMMARY OF THE INVENTION

14 Embodiments of the present invention provide methods for determining a path
15 through a network by grouping network structures into structure groups, determining virtual
16 circuit information for every pair of nodes in the structure groups, determining connections
17 between said structure groups, and determining a least cost path between the nodes using the
18 spare circuit information for the virtual circuit information and connection information. In
19 one embodiment, the virtual circuit information includes information for each structure
20 regarding whether a path using a common channel is available through the structure. In a
21 further embodiment, the virtual circuit information includes two pseudo nodes for each group
22 node. Methods of the present invention perform routing and channel assignment at the same
23 time because the present invention considers the availability of channels within a structure
24 group when determining the best path through the network. Use of the present invention
25 provides a more efficient capacity planning tool and/or capacity management tool that is able
26 to achieve real time network provisioning and just-in-time network capacity addition.

1 A further embodiment of the present invention determines a path through the network
2 by also considering requests for multiple time periods and multiple bandwidths. A further
3 embodiment may be used to plan paths that traverse, for example, SONET equipment,
4 Plesiochronous digital hierarchy (PDH) equipment, and a hybrid of these types of equipment.
5 In addition, one embodiment of the present invention also may be used to plan paths over a
6 hybrid of network topologies, such as mesh, linear chain, and close ring. A further
7 embodiment also considers requests for a certain bandwidth, routing preferences (e.g., ring
8 required, SONET required), and loading priority. In addition, one embodiment of the present
9 invention routes demand on a combination of express and local rings. A further embodiment
10 also considers planned bandwidth for a structure for multiple time periods by representing the
11 structure group as a slot-edge matrix for various time periods. This embodiment is used for
12 multi-period capacity planning of a network.

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14 **BRIEF DESCRIPTION OF THE DRAWINGS**

15 FIG 1 is a block diagram of a part of a network containing structure groups in
16 accordance with an embodiment of the present invention.

17 FIG 2 is a block diagram of a ring family in accordance with an embodiment of the
18 present invention.

19 FIG 3 is an equivalence graph showing virtual links for a structure group in
20 accordance with an embodiment of the present invention.

21 FIG 4 is a slot-edge matrix for a structure group in accordance with an embodiment of
22 the present invention.

23 FIG 5 is an equivalence graph showing pseudo links and pseudo nodes for a structure
24 group in accordance with a further embodiment of the present invention.

FIG 6 is a block diagram of four structure groups in accordance with an embodiment of the present invention.

FIG 7 is an equivalence graph showing pseudo links and pseudo nodes for four structure groups in accordance with an embodiment of the present invention.

FIG 8 is a flow diagram of a method for determining a path through the network in accordance with an embodiment of the present invention.

DETAILED DESCRIPTION

FIG 1 is a block diagram of a part of a network containing structure groups in accordance with an embodiment of the present invention. FIG 1 shows a portion of an optical network that contains group nodes 101, 102, 103, 104, 105, 106, 107, and 108. Each group node is connected to another node by a group link, such as the link 130 that connects group node 101 to group node 102. Each node contains at least an ADM. Node 120 is an office node and contains group node 103, group node 108, and cross-connect 109, which is also a group node. Each link may be bi-directional, in which case a signal may be sent across the link in either direction. For example, if the group link 130 is bi-directional, then a signal could be sent from group node 101 to group node 102 over group link 130, and a signal can be sent from node 102 to node 101 over link 130.

Group nodes 101, 102, 103, and 104 and the group links connecting them are part of structure group 141. Group nodes 105, 106, 107 and 108 and the group links connecting them are part of structure group 142. A structure group is a set of connected structures through which a signal can traverse without passing through a cross-connect. The structures in the same structure group may have the same set of ADM office locations. Typically, the structures in a structure group are all made by the same vendor and use the same protocol. A

1 structure group may be constructed using a ring topology, such as structure groups 141 and
2 142. Because ring groups have self healing capability, they are considered to be the most
3 reliable network topology for communication networks. A structure group may also be
4 constructed, for example, according to a point-to-point topology or a linear chain topology.
5 Linear chain topology, which is sometimes called an open ring, consists of a linear chain of
6 add/drop multiplexers connected by links. A point-to-point topology is the simplest topology
7 and consists of a linear chain with two nodes.

8 Ideally, a signal will be sent through a structure group on the same channel because a
9 cost is incurred whenever a signal changes from one channel to another channel. For
10 example, with reference to FIG 1, assume that a signal is being sent from node 101 to node
11 103. As discussed above, the high-speed signal sent over a network may be divided into a
12 number of lower-speed signals using time division multiplexing. In this case, each low-speed
13 signal sent from node 101 to node 104 occupies a particular time slot. For example, if there
14 were four time slots, a part of a first signal may be sent in slot 1, a part of a second signal may
15 be sent in slot 2, a part of a third signal may be sent in slot 3, and slot 4 may be unused. In
16 the next time frame, the next part of the first signal is sent in slot 1, the next part of the
17 second signal is sent in slot 2, ect. If the first time slot is available from node 101 to 104, and
18 the third time slot is available from node 104 to 103, a signal can be sent from node 101 to
19 103 using the first slot from node 101 to node 104 and using the third slot from node 104 to
20 103. To do so, the ADM at node 104 will have to drop out the signal from the first slot and
21 add it into the third slot. A cost is incurred whenever an ADM has to add or drop a signal
22 because of the ADM's low-speed plug-in and its associated cross-connected termination cost.

23 Whenever a signal leaves a structure group it must go through a cross-connect. Thus,
24 additional costs are incurred when changing from one channel to another channel. Similar

1 costs will be incurred whenever a signal traverses from one structure group to another
2 structure group.

3 FIG 2 is a block diagram of a ring family in accordance with an embodiment of the
4 present invention. A ring family contains the same set of office nodes. FIG 2 shows ring
5 family 200, which contains three structure groups, ring group RG1 with rings 220 and 230,
6 ring group RG2 with ring 210, and ring group RG3 with ring 240. A ring group may be a
7 stack of physical rings that reside on the same set of office nodes. Ring family 200 contains
8 office node 201, 202, 203 and 204. Ring family 200 also contains a set of rings 210, 220, 230
9 and 240. Rings 220 and 230 contain a set of links connecting office nodes 201, 202, 203 and
10 204. Ring 210 is the outermost ring, and ring 240 is the innermost ring. In the example
11 shown in FIG 2, rings 210, 230, and 240 may be able to support OC1/3/12/48 service, and
12 ring 220 may be able to support OC3 service. Ring 210 and 240 may be express rings which
13 do not stop at every node, while ring 220 and 230 may be local rings which stop at every
14 node.

15 FIG 3 is an equivalence graph showing virtual links for a structure group in
16 accordance with an embodiment of the present invention. Structure group 300 contains group
17 node 301, group node 302, group node 303 and group node 304. Structure group 300 may
18 correspond, for example, to structure group 141 of FIG 1, in which case group nodes 301,
19 302, 303 and 304 of FIG 3 may correspond to group nodes 101, 102, 103 and 104 of FIG 1.
20 In this embodiment, node 301 is connected to node 302 by a group link, node 302 is
21 connected to node 303 by a group link, node 303 is connected to node 304 by a group link,
22 and node 304 is connected to node 301 by a group link. The group links are not shown in
23 FIG 3. The lines shown in FIG 3 represent virtual links in a clockwise direction between
24 nodes in structure group 300. Virtual links may be used to represent connections that may be

1 made between two nodes in a structure group using one or more group links. The spare
2 circuit value for a virtual link represents the number of connections through a structure group
3 that may be made between two nodes without changing channels. Thus, the spare circuit
4 value for a virtual link is based on the number of channels available (i.e., spare) between two
5 nodes. For example, the solid line between node 301 and node 302 shows that the virtual link
6 between these nodes in a clockwise direction has a spare circuit value of 2. Thus, in this
7 example there are two channels available from node 301 to node 302 in a clockwise direction.
8 If the link is a bi-direction transmission, the spare channel from node 302 to node 301 in the
9 counter-clockwise direction is also value of 2. The dashed lines between adjacent nodes
10 represents the virtual links from the source node to the destination node in a clockwise
11 direction. For example, the dashed line from node 302 to node 301 represents the virtual link
12 from 302 → 303 → 304 → 301. The dashed line from node 302 to node 301 shows that the
13 virtual link from node 302 to node 301 in a clockwise direction has a spare circuit value of 0.

14 The dashed lines between node 301 and node 303 represent two virtual links from
15 node 301 to 303 and from 303 to 301, respectively. Similarly, the dashed lines between
16 nodes 302 and 304 represent two virtual links between node 302 and 304. The spare circuit
17 value of a virtual link between node 301 and node 303 in the clockwise direction is equal to
18 the number of channels that are available in both the link connecting node 301 to node 302
19 (in the clockwise direction) and the link connecting node 302 to node 303 (in the clockwise
20 direction) that meets the time slot assignment constraint in SONET ADM equipment, or
21 wavelength continuity constraint in optical ADM equipment. That is, the spare circuit value
22 is the number of channels available in both links at the same slot (channel). FIG 3 shows that
23 the virtual link from node 301 to node 303 in the clockwise direction has a spare circuit value
24 of 2.

1 FIG 4 is a slot-edge matrix for a structure group in accordance with an embodiment of
2 the present invention. The slot-edge matrix in FIG 4 contains edge (or physical link) usage
3 information regarding slots that are currently used or reserved for a given structure. This
4 information may be used to determine the spare circuit value for the virtual links in that
5 structure. According to an embodiment of the present invention, a network planning tool
6 maintains a slot-edge matrix for each structure in the network. In one embodiment, a slot-
7 edge matrix is maintained for each structure for various time periods. In a further
8 embodiment, where a signal in a structure uses more than one slot, a channel may comprise a
9 group of slots in the slot-edge matrix. For example, FIG 4 may be the slot-edge matrix in the
10 clockwise direction for an OC 48 closed ring with four nodes (which has 48 slots) and the
11 slots in FIG 4 may be grouped into channels of 12 slots each in order to accommodate signals
12 having a bandwidth of OC 12.

13 FIG 4 shows the channels available in the clockwise direction for edge 301-302, edge
14 302-303, edge 303-304, and edge 304-301. The edges in FIG 4 may represent the links in the
15 structure group of FIG 3. For example, edge 301-302 may represent the link connecting node
16 301 to node 302 in FIG 3. In FIG 4, the designation "unused" represents a set of slots that
17 are available, and the designation "used" represents that the slots are busy or reserved. For
18 example, FIG 4 shows that slots 1-12 are available in the clockwise direction along edge 301-
19 302 and edge 302-303, and that slots 1-12 are not available in the clockwise direction along
20 edge 303-304. In the example illustrated in FIG 4, the slots available in the clockwise
21 direction along edge 303-304 are slots 13-24, and the slots available in the clockwise
22 direction along edge 304-301 are slots 25-36 and slots 37-48. In view of this slot-edge
23 matrix, there are no virtual links in the clockwise direction from node 303 to node 301
24 because there is no channel that is available in the clockwise direction in both edge 303-304

1 and edge 304-301. Transmitting a signal in slots 13-24 along edge 303-304 and then in slots
2 25-36 (or 37-48) of edge 304-301 would be an expensive or sometime impossible hair pin
3 solution because the signal would have to be dropped and added in the new channel. The
4 impossibility may be due to the unavailability of low speed plug-in required for
5 dropping/adding signals by the ADM. The absence of any spare virtual circuits in the
6 clockwise direction along edge 303-301 may be represented in FIG 3 by a 0 accompanying
7 the dashed line from node 303 to node 301.

8 FIG 5 is an equivalence graph showing pseudo links and pseudo nodes for a structure
9 group in accordance with a further embodiment of the present invention. Pseudo nodes and
10 pseudo links may be created to differentiate between hair pin connections and non-hair pin
11 connections. For each group node in a structure group, two pseudo nodes are created, one
12 called an In-node and the other called an Out-node. In-nodes represent the node through
13 which traffic is being "Added in" for a structure group. Out-nodes represents the node that
14 traffic is to being "Dropped out" of a structure group. There are no connections from In-node
15 to In-node, from Out-node to Out-node, or from Out-node to In-node. A pseudo link is a
16 variation of a virtual link used to connect pseudo nodes.

17 FIG 5 shows a structure group 500 that includes group nodes 501, 502, 503, and 504.
18 Structure group 500 may correspond, for example, to structure group 141 of FIG 1, in which
19 case nodes 501, 502, 503 and 504 of FIG 5 may correspond to nodes 101, 102, 103 and 104
20 of FIG 1. Although the group links are not shown in FIG 5, in this embodiment node 501 is
21 connected to group node 502 by a group link, group node 502 is connected to group node 503
22 by a group link, group node 503 is connected to group node 504 by a group link, and group
23 node 504 is connected to group node 501 by a group link. In FIG 5, each group node is
24 represented as two pseudo nodes, one of which is a pseudo In-node and the other of which is

1 a pseudo Out-node. For example, node 501 is represented by pseudo node 501-In and pseudo
2 node 501-Out. The lines shown in FIG 5 represent pseudo links between the nodes in
3 structure group 500. For example, the line between pseudo node 501-In and pseudo node
4 502-Out is a pseudo link between node 501 and node 502, and the line between pseudo node
5 501-In and pseudo node 503-Out is a pseudo link between node 501 and node 503. D500
6 represents a group node that sends a signal (i.e., a demand for a given bandwidth) that enters
7 structure group 500 at pseudo node 502-In. D500 node also receives a signal from pseudo
8 node 502-Out.

9 The spare circuit values in FIG 5 (and FIG 3) are given by the clockwise view. If the
10 structure supports bi-directional transmission, then the number of spare circuits in the
11 counter-clockwise direction from a node i to node j can be derived from the spare circuits in
12 the clockwise direction from node j to node i. Thus, the counter-clockwise view can be
13 derived from the clockwise view.

14 The pseudo In-nodes in FIG 5 represent the entrance of a signal into structure group
15 500. For example, assume that structure group 500 is a SONET network using time division
16 multiplexing. Pseudo node 502-In may then represent the adding of a signal at node 502.
17 The pseudo Out-nodes in FIG 5 represent the exiting of a signal from structure group 500.
18 Using the example above, pseudo node 501-Out represents the dropping of a signal at node
19 501. In this embodiment, pseudo links are shown starting at a pseudo In-node because signals
20 enter the structure group at an In-node. Similarly, in this embodiment pseudo links are shown
21 ending at a pseudo Out-node because signals exit the structure group at an Out-node. A
22 pseudo link connecting a pseudo In-node to a pseudo Out-node represents a route for a signal
23 entering the system at the pseudo In-node and exiting the system at the pseudo Out-node.
24 One situation when a signal exits the system at a pseudo Out-node is when the signal changes

1 to another channel at that node. For example, the line from pseudo In-node 501 to pseudo
2 Out-node 502 represents a signal that enters the structure group at node 501 and exits at node
3 502, such as a signal that is dropped at node 502. The same signal may then re-enter the
4 structure group at pseudo node 502-In, for example if it is added back in another slot at node
5 502, and exit at pseudo node 503-Out. Each spare circuit for a pseudo link connecting two
6 nodes which are not adjacent with each other needs to meet the time slot assignment
7 constraint (i.e. the signal must stay in the same slot). For example, the pseudo link from
8 pseudo node 501-In to pseudo node 503-Out represents a link whose circuits need to travels
9 from node 501 to node 503 along the same channel. Such a signal, for example, passes
10 through node 502 without being added or dropped.

11 FIG 6 is a block diagram of four structure groups in accordance with an embodiment
12 of the present invention. Structure groups 600, 610, 620, and 630 in FIG 6 are shown at the
13 group node level. There are four structure groups, two at the west side (local group SG 600
14 and express group SG620) and two on the east side (local group SG610 and express group
15 SG630) of the center office node. Each local structure group has four group nodes, each of
16 which is represented by the designation L. Each express structure group has two nodes, both
17 of which are represented by the designation E. Structure group 600 contains pseudo nodes
18 L601, L602, L603 and L604. Similarly, structure group 610 contains pseudo nodes L605,
19 L606, L607, and L608. Express structure group 620 contains pseudo nodes E621 and E623.
20 Express structure group 630 contains pseudo nodes E636 and E637. Structure group 610 may
21 correspond to ring 230 of FIG 2 (the ring third from the outside) and structure group 620 may
22 correspond to ring 210 of FIG 2 (the outside ring). In this case, office node 201 of FIG 2 may
23 correspond to pseudo nodes L601 and E621, office node 202 may correspond to L602, office
24 node 203 may correspond to L603 and E623, and office node 204 may correspond to L604.

1 D651 represents a group node that sends a signal (i.e., a demand for a given
2 bandwidth) that enters structure group 600 or 620 at pseudo node L601 or E621. A demand
3 from D651 to point D652 can use 3 different possible types of paths: express (E621 → E623),
4 local (L601 → L602 → L603 or L601 → L604 → L603), and hair pin (L601 → L602 → hair
5 pin → L602 → L603, or L601 → L604 → hair pin → L604 → L603). A demand from D651
6 to D653 may have at least 9 possible paths. In practice, a network may have thousands of
7 structure groups and each structure group may have up to 16 nodes. Because of the
8 complexity of the route and slot assignment, two step algorithms have been used for solving
9 this problem.

10 FIG 7 is an equivalence graph showing pseudo links and pseudo nodes for four
11 structure groups in accordance with an embodiment of the present invention. FIG 7 shows
12 structure groups 700, 710, 720 and 730, which may correspond to structure groups 600, 610,
13 620 and 630 of FIG 6. Structure groups 700 and 710 are local, and structure groups 720 and
14 730 are express.

15 Each node in structure groups 700, 710, 720, and 730 is represented by pseudo In-
16 nodes and pseudo Out-nodes. An In-node corresponds to a route for a signal coming in to the
17 structure group at that node, and an Out-node corresponds to a route for a signal exiting the
18 structure group at that node. Express and local pseudo nodes correspond to express and local
19 routes. For example, structure group 700 includes group node 701, which is shown in FIG 7
20 as pseudo nodes L701in and L701out. Node 701 may correspond, for example, to node 101
21 of FIG 1 or node 201 of FIG 2. Pseudo node L701in corresponds to a signal that enters
22 structure group 700 at node 701 and travels on a local path. Pseudo node E721in represents a
23 signal that enters structure group 720 at node 721 and travels on an express path. Pseudo
24 node L701out represents a signal that travels to node 701 on a local path and exits structure

1 group 700 at node 701, and pseudo node E721out represents a signal that travels to node 721
2 on an express path and exits structure group 720 at node 721. If node L701 corresponds to a
3 node in RG1 (Ring Group 1) at office node 201 of FIG 2, then a signal entering structure
4 group 700 at pseudo node L701in and traveling along a local path to pseudo node L703out
5 corresponds to a signal entering node 201 and traveling along a path (e.g., ring 220 or 230) to
6 node 203. Alternatively, a path from D1 → E721in → E723out corresponds to a signal that
7 enters office node 201 at a node in RG2 of FIG 2 and travels along express ring 210 (the outer
8 ring) to office node 203.

9 There are no pseudo express nodes for node 702 because, in this example, an express
10 ring does not stop at node 702. If node 701 and 702 correspond to nodes 201 and 202 that
11 resides in RG1 of FIG 2, then a signal entering structure group 700 at pseudo node L701in
12 and traveling along a local path to pseudo node L702out corresponds to a signal that enters
13 node 201 of FIG 2 and travels along local ring 230 (the third ring from the center) to node
14 202. Structure group 700 in FIG 7 also includes pseudo nodes L703out, L703in, L704out,
15 and L704in. Structure group 710 includes pseudo nodes L705in, L705out, L706in, L706out,
16 L707in, L707out, L708in, and L708out. In FIG 7, D1 represents a digital cross-connect node
17 where a signal (i.e., a demand for a given bandwidth) may enter structure group 700 at node
18 701in or exit structure group 700 at 701out. D2 represents a signal that may enter or exit
19 structure group 700 at 703in or 703out, and may enter or exit structure group 710 at 708in or
20 708out. D2 may also enter or exit structure group 720 at node 723in or 723out, and may enter
21 or exit structure group 730 at node 737in or 737out. D3 represents a signal that may enter or
22 exit structure group 710 at node 706in or 706out, and may enter or exit structure group at
23 node 736in or 736out.

24 FIG 7 also shows certain spare circuit values for the pseudo links for structure group

1 700. The spare circuit values for structure group 700 shown in FIG 7 correspond to the spare
2 circuit values for structure group 300 shown in FIG 3, except that FIG 7 represents the nodes
3 as two pseudo nodes (In and Out). For example, the spare circuit value for the virtual link
4 from node 301 to node 302 in a clockwise direction is 2, and the spare circuit value for the
5 pseudo link for pseudo node L701in to L702out is also 2. Similarly, the spare circuit value
6 for the virtual link from node 302 to node 301 in a clockwise direction is 0, and the spare
7 circuit value for the pseudo link from pseudo node L702in to L701out is also 0. The spare
8 circuit value of virtual link from node 301 to 303, which equal 2, corresponds to the sum of
9 the spare circuit value from pseudo link L701in to L703out, which is 1, and the spare circuit
10 value from pseudo link E321 to E323, which is 1.

11 In this example, the least cost path from D1 to D2 is the express route, represented by
12 D1 → E721in → E723out → D2. The next least cost path is to use the local structure group
13 route without a hair pin, represented by D1 → L701in → L703out → D2. This route will go
14 through an intermediate ADM at its high speed. This intermediate ADM could be in L702 or
15 L704, depending on clockwise or counter-clockwise direction. That is, the route D1 →
16 L701in → L703out → D2 could go from L701in to L703out in the clockwise direction
17 (through L702) or in the counter-clockwise direction (though L704). The third possible route
18 will be one with a hair pin solution. This may be a route from D1 → L701 in → L702 out →
19 hair pin → L702in → L703 out → D2.

20 In one embodiment of the present invention, each node and link may be represented
21 by entries in a database. These values may be used by a network planning tool and/or
22 capacity management router to determine paths through the network. According to a further
23 embodiment, the spare circuit values for each structure group may be maintained in a data
24 base and used by the network planning tool and/or capacity management router. These values

1 may be, for example, the spare circuit values shown in FIG 3. In addition, these values may
2 be dynamically updated when a network planning tool allocates a path or when a capacity
3 management router accounts for a future path. In one embodiment, the database records the
4 spare circuit values for each time period, such as for example recording the spare circuit
5 values on a week-to-week or month-to-month basis. In a further embodiment, the spare
6 circuit values for each link may be associated with a priority. For example, paths that are
7 reserved on a high priority basis may be distinguishable from paths reserved on a low priority
8 basis.

9 In a further embodiment, the database may contain two pseudo node entries for each
10 group node. These entries may correspond, for example, to the pseudo node values shown in
11 FIG 5. In a further embodiment, the database distinguishes between local pseudo links and
12 express pseudo links, as for example is shown in FIG 7. In a further embodiment of the
13 present invention, the entry for each link in the database may be associated with a cost. Such
14 costs may be based, for example, on the length of the link or the remoteness of the area served
15 by the link. In a still further embodiment, the type of structure that a link resides in (e.g., ring,
16 linear chain, ect.) and the type of equipment used in the structure (e.g., SONET, PDH, ect.)
17 may be associated with the entry for the link.

18 FIG 8 is a flow diagram of a method for determining a path through the network in
19 accordance with an embodiment of the present invention. The method shown in FIG 8 could
20 be used, for example, by a network planning tool to plan a new path through a network from a
21 source to a destination. At 801, the structures in the network are grouped into structure
22 groups. At 802, a request for a path is received. This might be a request, for example, to use
23 an OC12 line from Fairfax, Virginia to New York City for a one-year period beginning three
24 months in the future. At 803, virtual circuit information for each structure group in the

1 network is determined. This information may include pseudo link and pseudo node
2 information for the structure group. At 804, the connection points between the structure
3 groups is determined. For example, with reference to FIG 7, the connection points between
4 structure groups 700, 710, 720 and 730 are determined (e.g., D1, D2, D3). At 805, a least
5 cost path from the source to the destination is determined using the virtual circuit
6 information. The least cost path may be determined, for example, using a Dijkstra shortest
7 path algorithm with cost as the "distance metric". The Dijkstra algorithm is a well known
8 algorithm for finding the shortest path from a source to a destination, and it is discussed for
9 example in Daniel C. Lynch & Marshall T. Rose, INTERNET SYSTEMS HANDBOOK 165-169
10 (1993). The Dijkstra algorithm is a recursive algorithm that uses a data base representing the
11 costs for traversing a network. The method of the present invention may also be used with
12 other shortest path algorithms, such as depth first or breadth first algorithms.

13 In one embodiment, there is a cost associated with each pseudo link and pseudo node
14 visited on a potential path. In this embodiment, the least cost algorithm considers the costs
15 associated with each pseudo link and pseudo node. The cost of a link may be related to the
16 length of the link. In a further embodiment, the cost of a pseudo link is increased for a
17 potential path if the signal must change channels at that link. The channel may be, for
18 example, a time slot channel or a frequency channel. The availability of a channel may be
19 determined based on the slot-edge matrix for a structure group. In a further embodiment, the
20 cost of an express link is less than the cost of an local link. In another embodiment, a request
21 for a path may specify the type of equipment requested, and the least cost algorithm considers
22 the type of equipment requested when determining a path through the network. In another
23 embodiment, a request for a path may specify a bandwidth requested, and the least cost path
24 algorithm considers the bandwidth requested when determining a path. In a further

1 embodiment, the slot-edge matrixes are updated to reflect that the new path has been
2 reserved.

3 After step 805 in FIG 8, a new request for a path through a network may be processed
4 at step 802.

5 Embodiments of the present invention provide methods for efficiently considering the
6 availability of channels and the cost of channel interchange when determining the best path
7 through a network. In one embodiment, a network is grouped into structure groups, the
8 number of spare circuits on each pseudo link between every two nodes in a structure group is
9 determined, and a least cost path between the nodes using the spare circuit information for the
10 pseudo links is determined. The methods of the present invention may be used in a network
11 planning tool to perform capacity planning at a systems level of traffic on a network. Because
12 methods of the present invention perform routing and channel assignment at the same time, a
13 network planning tool using the present invention more efficiently achieves routing and
14 channel assignment. For example, when implemented on a UNIX based workstation with 2
15 Giga bytes or memory, a network planning tool that uses the present invention can route and
16 assign the slot for over 5,000 STS1/3/12/48 demands onto a network transport model within
17 one hour.

18 Several embodiments of the present invention are specifically illustrated and/or
19 described herein. However, it will be appreciated that modifications and variations of the
20 present invention are covered by the above teachings and within the purview of the appended
21 claims without departing from the spirit and intended scope of the invention. For example,
22 although embodiments relate channels to slots in the time division multiplexing context, a
23 channel is not limited to slots. For example, a channel may also relate to a frequency in the
24 wavelength division mutliplexing context. In addition, although embodiments show structure

- 1 groups having a ring topology, the methods disclosed herein may also be used with other
- 2 topologies, such as linear chain.